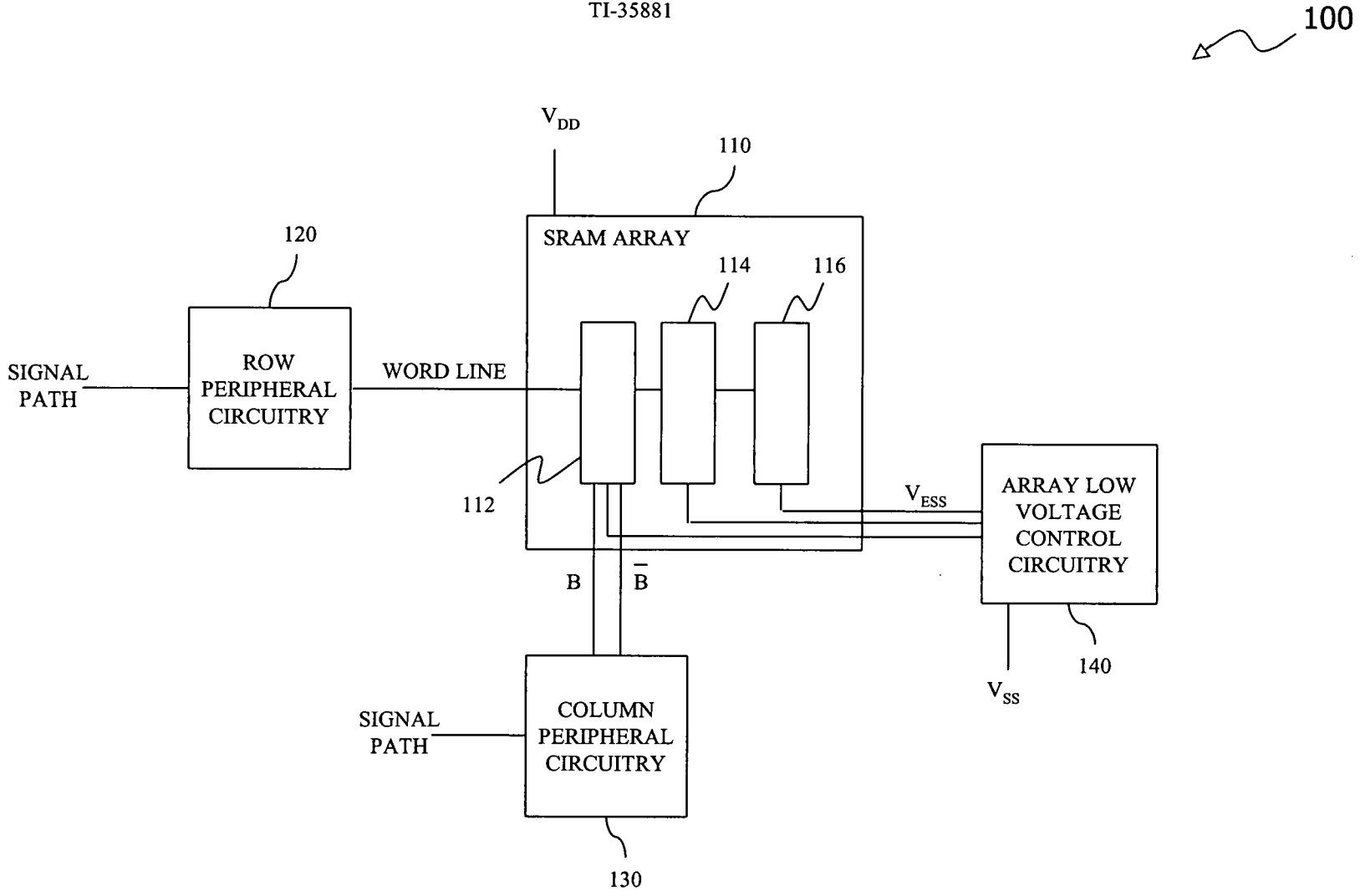
100
V_{DD}
110
SRAM ARRAY
120
114
116
SIGNAL PATH
ROW PERIPHERAL CIRCUITRY
WORD LINE
112
V_{ESS}
ARRAY LOW VOLTAGE CONTROL CIRCUITRY
B
$\overline{B}$
140
V_{SS}
SIGNAL PATH
COLUMN PERIPHERAL CIRCUITRY
130

FIGURE 1

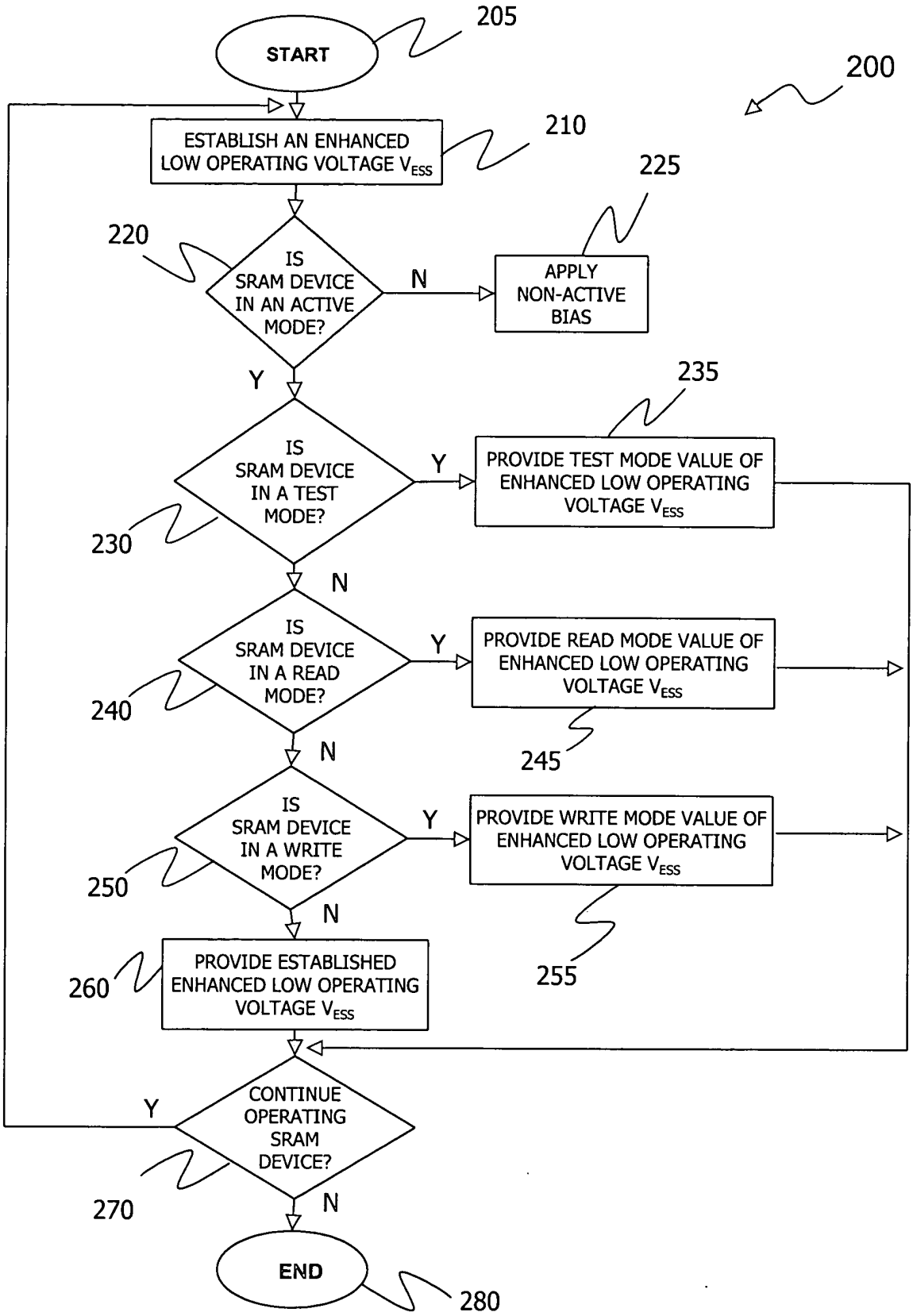
205
START
200
ESTABLISH AN ENHANCED LOW OPERATING VOLTAGE V_{ESS}
210
225
220
IS SRAM DEVICE IN AN ACTIVE MODE?
N
APPLY NON-ACTIVE BIAS
Y
235
IS SRAM DEVICE IN A TEST MODE?
Y
PROVIDE TEST MODE VALUE OF ENHANCED LOW OPERATING VOLTAGE V_{ESS}
230
N
IS SRAM DEVICE IN A READ MODE?
Y
PROVIDE READ MODE VALUE OF ENHANCED LOW OPERATING VOLTAGE V_{ESS}
240
245
N
IS SRAM DEVICE IN A WRITE MODE?
Y
PROVIDE WRITE MODE VALUE OF ENHANCED LOW OPERATING VOLTAGE V_{ESS}
250
255
N
PROVIDE ESTABLISHED ENHANCED LOW OPERATING VOLTAGE V_{ESS}
260
CONTINUE OPERATING SRAM DEVICE?
Y
270
N
END
280

FIGURE 2